

IN THE DRAWINGS

As noted by Examiner, Applicant has amended FIG. 3 such that lines **362** and **364** connected between the load logic **202** and the shift registers **304** and **306** provide proper arrow flow indicators as described in paragraph **[0048]**.

As noted by Examiner, Applicant has amended FIG. 6 to now provide reference **606** in connection with the decision element of ““1” Chip or “0” Chip?” as noted in paragraph **[0072]**.

As the final version of the original drawings was not available, Applicant has re-generated all drawings for consistency in font and text.

No new matter has been added by these amendments.